

TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY, WARANANAGAR

TIME TABLE

M. Tech Electronics and Telecommunication (SEMESTER 1)

ACADEMIC YEAR-2021/2022



SR. NO.	TIME	MON	TUE	WED	THU	FRI	SAT
1	9.00	RANDOM PROCESS	ADVANCE EMBEDDED SYSTEM	ADVANCE WIRELESS COMMUNICATION	MOBILE COMPUTING	MOBILE COMPUTING	RANDOM PROCESS
2	10.00	RANDOM PROCESS	ADVANCE EMBEDDED SYSTEM	ADVANCE EMBEDDED SYSTEM	MOBILE COMPUTING	ADVANCE WIRELESS COMMUNICATION	ERROR CONTROL CODING TECHNIQUE
	11.00	TEA BREAK					
3	11.15		Laboratory Practice Practical-1	-----	Laboratory Practice Practical-1	ERROR CONTROL CODING TECHNIQUE	ADVANCE WIRELESS COMMUNICATION
4	12.15	ERROR CONTROL CODING TECHNIQUE				ADVANCE EMBEDDED SYSTEM-T	ERROR CONTROL CODING TECHNIQUE-T
	1.15	LUNCH BREAK					
5	2.15	SEMENAR -I	---				
6	3.15		---				

1. Prof. D. G. Chougule
3. Prof. B. T. Salokhe
5. Prof. S. S. Patil

- Advance Embedded System
- Mobile Computing
- Error Control Coding Technique

2. Prof. B. G. Warvante
4. Prof. A. S. Mali

- Advance Wireless Communication
- Random Process

PG COORDINATOR
Prof. Abhijit S. Mali

HOD
Prof. C. P. Shinde

Principal
PRINCIPAL
Dr. S. V. Anekar

TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY, WARANANAGAR

TIME TABLE

M. Tech Electronics and Telecommunication (SEMESTER 3)

ACADEMIC YEAR-2021/2022



SR. NO.	TIME	MON	TUE	WED	THU	FRI	SAT
1	9.00						
2	10.00						
	11.00	TEA BREAK					
3	11.15					Industrial Training	
4	12.15						
	1.15	LUNCH BREAK					
5	2.15		Industrial Training	DISSERTATION PHASE-1	DISSERTATION PHASE-1	Research Methodology	
6	3.15						

1. Prof. D. G. Chougule
3. Prof. A. S. Mali

2. Prof. B. T. Salokhe
4. Prof. S. S. Patil

PG COORDINATOR
Prof. Abhijit S. Mali

HOD
Prof. C. P. Shinde

PRINCIPAL
Dr. S. V. Anekar