

SV - 621

Total No. of Pages : 2

Seat No.	
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**F.Y. B.Tech. (Part - I) (Semester - I) (Revised) Examination,
May - 2019**

FUNDAMENTALS OF ELECTRONICS AND COMPUTERS

Sub. Code : 71818

Day and Date : Wednesday, 08 - 05 - 2019

Total Marks : 70

Time : 10.00 a.m. to 12.30 p.m.

- Instructions :**
- 1) Solve any three questions from Section I and Section II.
 - 2) Figure to right indicates full marks.
 - 3) Assume suitable data if necessary.

SECTION - I

Q1) Solve the following. [12]

- a) Explain FW rectifier using centre tap transformer with necessary wave-forms.
- b) Explain i/p and o/p characteristics for CE configuration.

Q2) Solve the following. [11]

- a) State and explain the De'Morgans laws
- b) What is De-multiplexer? Explain 1:4 De-mux with truth table.

Q3) Solve the following. [11]

- a) Explain full adder circuit with truth table.
- b) Explain the working of digital thermometer

Q4) Solve the following. [12]

- a) Explain displacement transducer. What is LVDT transducer?
- b) Explain washing machine with block diagram.

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SECTION - II

- Q5) a)** With a neat labeled diagram, Explain different components of Computer. [6]
b) Enlist and elaborate any six applications of computers. [6]
- Q6) a)** Explain following linux commands [6]
i) Ls
ii) cd
iii) mkdir
b) What is system software? Explain any one in detail. [5]
- Q7) a)** What is a flow chart? Draw a flowchart to read "n" numbers and add them. [6]
b) With neat diagram, briefly Explain OSI reference model? [5]
- Q8) a)** Convert number system [6]
i) 111101.0110 Binary to Decimal
ii) 4B27 Hexadecimal to Decimal
iii) 174 Decimal to Binary
b) Explain different types of operating system. [6]

