

SWVSM's  
**TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY,  
WARANANAGAR**

(An Autonomous Institute affiliated to Shivaji University)

<b>BOARD OF STUDIES</b>
-------------------------

**Term: 03 Years**

**Date of Initial Composition: 8<sup>th</sup> Feb 2021**

**Frequency: 01 Meeting /Semester**

**The functions of Board of Studies**

- To prepare syllabi for various course keeping in view the objectives of the Institute, interest of stake holders and national requirement for consideration and approval of the Academic council.
- To suggest methodologies for innovative teaching and evaluation techniques.
- To suggest panel of names to the Academic Council for appointment of examiners
- To coordinate research, teaching, extension and other Academic activities in the department / Institute.

**BOARD OF STUDIES --- DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING**

Sr. No.	Composition	Name & Affiliation	Nominated By	Remark
1.	Head of Department	Prof. A. G. Patil	Principal	Chairman
2.	Member from Senior Faculty within Department (Entire Faculty of Each specialization)	Dr. G. V. Patil	HoD/ Chairman	Member
		Prof. A. S. Phalle		Member
		Prof. R. B. Patil		Member
		Prof. D. B. Mirajkar		Member
		Prof. K. K. Awale		Member
		Prof. S. R. Shetake		Member
		Prof. Mrs. B. A. Chougule		Member
		Prof. M. R. Hudagi		Member
		Prof. S. A. Urabinahatti		Member
		Prof. R. C. Shikkeri		Member
3.	Two Subject Experts from outside the parent University	Dr. S. L. Deshpande <i>HoD, CNE VTU, Belgavi</i>	Academic Council	Member
		Dr.(Mrs.) V.S. Inamdar <i>Associate Professor, College of Engineering, Pune</i>		Member
4.	One Expert from the panel of 6 recommended members by the College Principal	Dr. D.V. Kodavade <i>Head of Department &amp; Professor in Computer Sc. &amp; Engineering, DKTE, Ichalkaranji</i>	Vice Chancellor, Shivaji University, Kolhapur	Member
5.	One representative from Industry /Corporate sector/allied area related to the placement	Mr. Dinesh Kothavade <i>Project Manager, TCS Pune</i>	Principal/ HoD /TPO	Member
		Mr. Hrishikesh Hirve <i>Senior Manager, Capgemini</i>		Member
		Mr. Nilesh Ratnaparkhi <i>CTO, iauro Systems Pvt.Ltd Pune</i>		Member
6.	One PG meritorious Alumnus	Dr. Sachin Patil <i>Assistant Professor, Rajarambapu Institute of Technology, Rajaramnagar.</i>	Principal/HoD	Member Expert from outside the college whenever special courses of studies are to be formulated
	Member of staff	Prof. I. I. Tamboli		Member Other Member of staff the same faculty

HoD, CSE

**Department of Computer Science and Engineering**  
**Board of Studies**  
**Meeting 1**

09-April-2021

**NOTICE**

All the Honourable members of the Board of Studies of Computer Science and Engineering department are hereby informed that due to Covid-19 pandemic, the First (01) BoS meeting is convened through online platform. Therefore, you are humbly requested to make yourself available and attend the same.

**Day & Date** :- Tuesday, 20-April-2021  
**Time** :- 10:30 AM - 12:30 PM  
**Mode of Meeting** :- Online (Google Meet)  
**Veune** :- Conference Hall

**Agenda**

- 1.1 Greeting and Welcoming the BoS members
- 1.2 Introduction of the BoS Members
- 1.3 Discuss the objectives of BoS and role of BoS members
- 1.4 Review of existing **B.Tech (CBCS) Shivaji University** syllabus structure
- 1.5 Presentation of proposed **B.Tech (Autonomous)** syllabus structure w.e.f 2021-22
- 1.6 Inviting suggestions from BoS members for proposed syllabus structure
- 1.7 Recommendation of text books and reference books
- 1.8 Methodologies for teaching-learning and evaluation
- 1.9 Approval of S.Y.B.Tech syllabus structure to be implemented w.e.f 2021-22
- 1.10 Any other point with the permission of the Chair

**Prof. R.B. Patil**  
BoS, Member Secretary

**Prof. A.G.Patil**  
BoS Chairman & HoD, CSE

**Department of Computer Science and Engineering**  
**Board of Studies (Autonomous)**  
**Meeting 1**

09-April-2021

**NOTICE**

All the faculty members of the Computer Science and Engineering department are hereby informed that, the First (01) Autonomous Board of Studies meeting is convened on Tuesday, 20<sup>th</sup> April 2021.

Therefore, you are humbly requested to make yourself available and attend the same.

**Day & Date** :- Tuesday, 20-April-2021  
**Time** :- 10:30 AM - 12:30 PM  
**Mode of Meeting** :- Online (Google Meet) for external BoS members  
**Veune** :- Conference Hall

**Agenda**

- 1.1 Greeting and Welcoming the BoS members
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**Prof. A.G.Patil**  
BoS Chairman & HoD, CSE

SWVSM's  
**Tatyasaheb Kore Institute of Engineering  
and Technology, Warananagar** Tal-Panhala Dist-Kolhapur 416113 (Maharashtra)  
(An Autonomous Institute, Affiliated to Shivaji University, Kolhapur)

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**Department of Computer Science and Engineering**  
**Board of Studies (Autonomous)**  
**Meeting -- 01**

**Day & Date** :- Tuesday, 20-April-2021  
**Time** :- 10:00 AM - 12:00 PM  
**Mode of Meeting** :- Online (Google Meet) for external BoS members  
**Veune** :- Conference Hall

**Table Program**

S.N	Activity	Coordinator / Speaker	Duration (in Mins)
1.	Welcome Speech	Prof. I. I. Tamboli	10:00 AM - 10:02 AM (2 Mins)
2.	Introduction of External BoS Members	Dr. G. V. Patil	10:02 AM - 10:05 AM (3 Mins)
3.	Introduction of Internal BoS Members	Self Introduction	10:05 AM - 10:11 AM (6 Mins)
4.	Address by Principal	Dr. S. V. Anekar	10:11 AM - 10:16 AM (5 Mins)
5.	Address by Dean Academics	Dr. K. I. Patil	10:16 AM - 10:21 AM (5 Mins)
6.	Address by BoS Chairman (Objective of Meeting, Role of BoS member)	Prof. A. G. Patil	10:21 AM - 10:26 AM (5 Mins)
7.	Review of existing <b>B.Tech (CBCS) Shivaji University</b> syllabus structure	Prof. R. B. Patil	10:26 AM - 10:31 AM (5 Mins)
8.	Presentation of proposed <b>B.Tech (Autonomous)</b> syllabus structure w.e.f 2021-22	Prof. R. B. Patil	10:31 AM - 10:46 AM (15 Mins)
9.	Inviting suggestions from BoS External members	Open forum	10:46 AM - 11:11 AM (25 Mins)
10.	Recommendation of text books and reference books	Open forum	11:11 AM - 11:21 AM (10 Mins)
11.	Methodologies for teaching-learning and evaluation	Open forum	11:21 AM - 11:31 AM (10 Mins)
12.	Vote of Thanks	Prof. Sachin A. U.	11:31 AM - 11:33 AM (2 Mins)
Documentation of Minutes of Meeting		Prof. M. R. Hudagi & Prof. Mrs. B. A. Chougule	

**Prof. R.B. Patil**  
BoS, Member Secretary

**Prof. A. G. Patil**  
BoS Chairman & HoD, CSE

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Ref

7-April-2021

To,  
Hon'ble BoS Member,

**Subject:** Invitation for Board of Studies Meeting No.1

Dear Sir,  
Greetings from CSE Department and TKIET, Warananagar !!!

With reference to above mentioned subject, we are glad to invite you for the 1<sup>st</sup> meeting of the Board of Studies through the online mode. Therefore, you are humbly requested to make yourself available to attend the meeting as a BoS Member and guide us.

Kindly acknowledge the same.

Meeting details are as follows:

<b>Day &amp; Date</b>	: - Tuesday, 20-April-2021
<b>Time</b>	: - 10:30 AM - 12:30 PM
<b>Mode of Meeting</b>	: - Online (Google Meet)

### Agenda

- 1.1 Greeting and Welcoming the BoS members
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- 1.10 Any other point with the permission of the Chair

**Prof. R.B. Patil**  
BoS, Member Secretary

**Prof. A.G.Patil**  
BoS Chairman & HoD, CSE

SWVSM's  
**Tatyasaheb Kore Institute of Engineering  
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**Department of Computer Science and Engineering**  
**Board of Studies (Autonomous)**  
**Meeting - 01 (First)**

**Day & Date** :- Tuesday, 20-April-2021  
**Time** :- 10:30 AM - 12:30 PM  
**Mode of Meeting** :- Online (Google Meet) for external BoS members  
**Veune** :- Conference Hall

**Table Program**

S. N.	Activity	Coordinator / Speaker	Duration (in Mins)
1.	Welcome Speech	Prof. I. I. Tamboli	10:30 AM - 10:35 AM (5 Mins)
2.	Introduction of External BoS Members	Dr. G. V. Patil [Dean, Students]	10:35 AM - 10:45 AM (10 Mins)
3.	Address by Principal	Dr. S. V. Anekar	10:45 AM - 10:50 AM (5 Mins)
4.	Address by Dean Academics	Dr. K. I. Patil	10:50 AM - 10:55 AM (5 Mins)
5.	Address by BoS Chairman (Dept. Vision-Mission, Intro. Internal of BoS Members & Roles and Objectives of BoS)	Prof. A. G. Patil [BoS Chairman & HoD]	10:55 AM - 11:05 AM (10 Mins)
6.	Review of existing B.Tech (CBCS) Shivaji University syllabus structure	Prof. R. B. Patil [BoS, Member Secretary]	11:05 AM - 11:10 AM (5 Mins)
7.	Presentation of proposed B.Tech (Autonomous) syllabus structure w.e.f 2021-22	Prof. R. B. Patil [BoS, Member Secretary]	11:10 AM - 11:25 AM (15 Mins)
8.	Inviting suggestions from BoS External members	Open Forum [Prof. R. B. Patil]	11:25 AM - 11:50 AM (25 Mins)
9.	Recommendation of text books and reference books	Open Forum [Prof. R. B. Patil]	11:50 AM - 11:55 AM (5 Mins)
10.	Ultra-Modern Teaching-Learning Methodologies	Open forum [Prof. R. B. Patil]	11:55 AM - 12:05 PM (10 Mins)
11.	Consent of Approval of S.Y.B.Tech Syllabus Structure	Prof. A. G. Patil [BoS Chairman & HoD]	12:05 PM - 12:10 PM (5 Mins)
12.	Any other issue, with the permission of the chair	Prof. R. C. Shikkeri [Asst. TPO & BoS Member]	12:10 PM - 12:15 PM (5 Mins)
13.	Vote of Thanks	Prof. R. C. Shikkeri [Asst. TPO & BoS Member]	12:15 PM - 12:25 PM (10 Mins)
14.	Documentation of Minutes of Meeting	Prof. M. R. Hudagi & Prof. Mrs. B. A. Chougule	

**Prof. R.B. Patil**  
BoS, Member Secretary

**Prof. A. G. Patil**  
BoS Chairman & HoD, CSE





Shree Warana Vibhag Shikshan Mandal's

# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

WARANANAGAR, Pin- 416113. Tal. Panhala, Dist. Kolhapur, M.S., India.

Phone : 02328-224012, 224013, Fax : 02328-223507

E-mail : principal@tkietwarana.ac.in Website : www.tkietwarana.ac.in

An ISO 9001 : 2000 Certified Institute ; Accredited by National Board of Accreditation, New Delhi : NACC Accredited A- Grade (3.27 CGPA)

Recognized by Govt. of Maharashtra ; Approved by A.I.C.T.E., New Delhi and Affiliated to Shivaji University, Kolhapur

## An Autonomous Institute

Ref. TKIET/ 2020-21/Autonomous/350

Date: 20-03-2021

To

**Dr. S. L. Deshpande**

HoD, CNE

VTU, Belgavi

**Subject:** Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

Respected Sir,

**Greetings from Department of Computer Science & Engineering and TKIET, Warananagar...!!!**

The Institute was established in 1983 by visionary leader Late Shri.Tatyasaheb Kore, an architect of Warana cooperative and educational movement. TKIET has been accredited twice by NBA, New Delhi and has secured 'A' grade from NAAC, Bangalore with CGPA of 3.27. Recently, academic autonomous status is also conferred to Institute under Shivaji University by UGC, New Delhi on December 2020.

As per the guideline of UGC, Board of Studies is required to constitute for respective programs in the Autonomous Institute. It is integral part of autonomous institute which will give direction to set quality curriculum and future of department. The BoS plays a pivotal role in academics with the following functions.

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We are pleased to inform you that you have been nominated by our Institute and very much interested to receive your scholarly guidance as member for BoS of Computer Science and Engineering department .We strongly believe that our Department will nurture under your direction and experience. Hence we kindly request you accept and give your consent for membership. Your valuable guidance and suggestions will surely enhance the curriculum standards.

Thanking You

Dean Academics

BoS Chairman

Principal





Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

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## Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'

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**Santosh Deshpande** <sladeshpande@gmail.com>

Sat, Mar 20, 2021 at 10:03 PM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Thanks a lot sir I will sincerely give my best and acknowledge your mail as my humble acceptance  
Thanks again

Sent from my iPhone

On 20-Mar-2021, at 11:18 AM, Head, Department of Computer Science and Engineering  
<hodcse@tkietwarana.ac.in> wrote:

[Quoted text hidden]

<BoS Invitation\_Dr.S.L.Deshpande.pdf>



Shree Warana Vibhag Shikshan Mandal's

# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

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## An Autonomous Institute

Ref. TKIET/ 2020-21/Autonomous/350

Date: 20-03-2024

To

**Dr.(Mrs.) V.S. Inamdar**

Associate Professor,  
College of Engineering, Pune  
Shivajinagar, Pune-411 005.

**Subject:** Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

**Respected Madam,**

**Greetings from Department of Computer Science & Engineering and TKIET, Warananagar...!!!**

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Thanking You

Dean Academics

BoS Chairman

Principal



Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

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## Regarding Consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'

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vhj.comp@coep.ac.in <vhj.comp@coep.ac.in>

Sat, Mar 20, 2021 at 12:23 PM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Cc: Principal TKIET <principal@tkietwarana.ac.in>, "deanacademic@tkietwarana.ac.in"

<deanacademic@tkietwarana.ac.in>, "Ganesh V. Patil" <gvpatil@tkietwarana.ac.in>, "Ramgonda B. Patil" <rbpatil@tkietwarana.ac.in>

Dear sir

Thank you for nominating me as a member of board of study for the computer Engineering programme.

Herewith I give my consent for the same.

Framing the curriculum is one of the very important activity in autonomous institute as these budding engineers will be contributing for the nation building.

It is my pleasure to contribute for the same.

Thanks and regards

Dr. Ms.Vandana Inamdar  
Associate Professor in Computer Engineering  
Department of Computer Engineering and Information Technology  
College of Engineering  
Pune-411005  
Tel. - 02025507093  
cell. - 9881688367

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**From:** Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

**Sent:** Saturday, March 20, 2021 5:38 AM

**To:** [vhj.comp@coep.ac.in](mailto:vhj.comp@coep.ac.in) <vhj.comp@coep.ac.in>; [inamdar.vandana6@gmail.com](mailto:inamdar.vandana6@gmail.com) <inamdar.vandana6@gmail.com>

**Cc:** Principal TKIET <principal@tkietwarana.ac.in>; [deanacademic@tkietwarana.ac.in](mailto:deanacademic@tkietwarana.ac.in) <deanacademic@tkietwarana.ac.in>; Ganesh V. Patil <gvpatil@tkietwarana.ac.in>; Ramgonda B. Patil <rbpatil@tkietwarana.ac.in>

**Subject:** Regarding Consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'

[Quoted text hidden]





Shree Warana Vibhag Shikshan Mandal's

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## AN AUTONOMOUS INSTITUTE

Ref. TKIET/ 2020-21/Autonomous/ BOS/281

Date: 6-3-2021

To

Dr. D.V.Kodavade

D.K.T.E.

Ichalkarangi

**Subject:** Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science & Engineering Department of TKIET under Autonomous Status'.

Ref.No.: SU/ affiliation/T-2/no.00049 dated 2<sup>nd</sup> March 2021

Respected Sir,

Greetings from Department of Computer Science & Engineering and TKIET, Warananagar.....!!!

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We are pleased to inform you that your name has been nominated by Shivaji University Kolhapur and very much interested to receive your scholarly guidance as member for BoS of Computer Science & Engineering department. We strongly believe that our Department will nurture under your direction and experience. Hence we finally request you accept and give your consent for membership. Your valuable guidance and suggestions will surely enhance the curriculum standards.

Thanking You

  
Dean Academics

  
BoS Chairman

  
Principal,



Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

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## Invitation as a member on BoS of Department of CSE - TKIET Warananagar.

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Dr. Dattatraya Vishnu Kodavade <dvkodavade@gmail.com>

Sat, Mar 6, 2021 at 2:33 PM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Dear sir,  
Thank you for your mail and invitation. I herewith accept the invitation.  
Regards,  
[Quoted text hidden]





Shree Warana Vibhag Shikshan Mandal's

# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

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**An Autonomous Institute**

Ref. TKIET/ 2020-21/Autonomous/ 350

Date: 20-03-2021

To

**Mr. Dinesh Kothavade**  
Project Manager,  
TCS Pune.

**Subject:** Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

Respected Sir,

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Thanking You

  
Dean Academics

  
BoS Chairman

  
Principal



Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

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## Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

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Dinesh Kothawade <dinesh.kothawade@tcs.com>

Tue, Mar 30, 2021 at 12:08 AM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Cc: deanacademic@tkietwarana.ac.in, "Ganesh V. Patil" <gvpatil@tkietwarana.ac.in>, HRISHIKESH DHANDE <hrishikesh.dhande@tcs.com>, "Ramgonda B. Patil" <rbpatil@tkietwarana.ac.in>, "Rajkumar C. Shikkeri" <rcshikkeri@tkietwarana.ac.in>

Thanks for this invite

I confirmed my acceptance for this Period

Thanks and regards

Dinesh Kothawade

SAP Service Delivery Manager

Tata Consultancy Services || TCS Sahyadri Park Hinjewadi PH - 3||

Pune|| Work: +91 20 64946023 I I Cell: +91 9833155801

Email: [Dinesh.Kothawade@tcs.com](mailto:Dinesh.Kothawade@tcs.com)



LEADING CHANGE  
INTEGRITY  
RESPECT FOR INDIVIDUAL  
EXCELLENCE  
LEARNING AND SHARING

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From: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

To: "Dinesh Kothawade" <dinesh.kothawade@tcs.com>

Cc: "HRISHIKESH DHANDE" <hrishikesh.dhande@tcs.com>, "Ganesh V. Patil" <gvpatil@tkietwarana.ac.in>, deanacademic@tkietwarana.ac.in, "Ramgonda B. Patil" <rbpatil@tkietwarana.ac.in>, "Rajkumar C. Shikkeri" <rcshikkeri@tkietwarana.ac.in>

Date: 03/22/2021 03:35 PM

Subject: Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

---

### "External email. Open with Caution"

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[Quoted text hidden]

TKIET, Warananagar[attachment "Dinesh Kothavade Updated.jpg" deleted by Dinesh Kothawade/MUM/TCS]

=====  
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# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

WARANANAGAR, Pin- 416113. Tal. Panhala, Dist. Kolhapur, M.S., India.

Phone : 02328-224012, 224013, Fax : 02328-223507

E-mail : principal@tkietwarana.ac.in Website : www.tkietwarana.ac.in

An ISO 9001 : 2000 Certified Institute ; Accredited by National Board of Accreditation, New Delhi : NACC Accredited A- Grade (3.27 CGPA)  
Recognized by Govt. of Maharashtra ; Approved by A.I.C.T.E., New Delhi and Affiliated to Shivaji University, Kolhapur

## An Autonomous Institute

Ref. TKIET/ 2020-21/Autonomous/350

Date: 20-03-2021

To

**Mr. Hrishikesh Hirve**

Senior Manager,  
Capegemini, Pune

**Subject:** Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

Respected Sir,

**Greetings from Department of Computer Science & Engineering and TKIET, Warananagar...!!!**

The Institute was established in 1983 by visionary leader Late Shri.Tatyasaheb Kore, an architect of Warana cooperative and educational movement. TKIET has been accredited twice by NBA, New Delhi and has secured 'A' grade from NAAC, Bangalore with **CGPA of 3.27**. Recently, academic autonomous status is also conferred to Institute under Shivaji University by UGC, New Delhi on December 2020.

As per the guideline of UGC, Board of Studies is required to constitute for respective programs in the Autonomous Institute. It is integral part of autonomous institute which will give direction to set quality curriculum and future of department. The BoS plays a pivotal role in academics with the following functions.

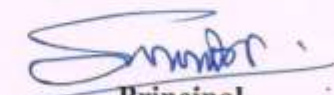
- To prepare syllabi for various course keeping in view the objectives of the Institute, interest of stake holders and national requirement for consideration and approval of the Academic council.
- To suggest methodologies for innovative teaching and evaluation techniques.
- To suggest panel of names to the Academic Council for appointment of examiners
- To coordinate research, teaching, extension and other Academic activities in the department / Institute.

We are pleased to inform you that you have been nominated by our Institute and very much interested to receive your scholarly guidance as member for BoS of Computer Science and Engineering department .We strongly believe that our Department will nurture under your direction and experience. Hence we kindly request you accept and give your consent for membership. Your valuable guidance and suggestions will surely enhance the curriculum standards.

Thanking You

  
Dean Academics

  
BoS Chairman

  
Principal

---

## Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'

---

**Hrishikesh Hirve** <hrishikesh.hirve@gmail.com>

Mon, Mar 22, 2021 at 8:27 AM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Cc: Principal TKIET <principal@tkietwarana.ac.in>, deanacademic@tkietwarana.ac.in, "Ganesh V. Patil" <gvpatil@tkietwarana.ac.in>, "Ramgonda B. Patil" <rbpatil@tkietwarana.ac.in>

Respected sir,

Please consider this email as my consent for the role and responsibilities for the board member.

Thank you for considering me for this opportunity.

Thanks & Regards,  
Hrishikesh Vijay Hirve

[Quoted text hidden]





Shree Warana Vibhag Shikshan Mandal's

# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

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Recognized by Govt. of Maharashtra ; Approved by A.I.C.T.E., New Delhi and Affiliated to Shivaji University, Kolhapur

## An Autonomous Institute

Ref. TKIET/ 2020-21/Autonomous/350

Date: 20.03.2021

To

**Mr. Nilesh Ratnaparkhi**  
Chief Technology Officer,  
iauro Systems Pvt.Ltd , Pune

**Subject:** Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

Respected Sir,

**Greetings from Department of Computer Science & Engineering and TKIET, Warananagar...!!!**

The Institute was established in 1983 by visionary leader Late Shri.Tatyasaheb Kore, an architect of Warana cooperative and educational movement. TKIET has been accredited twice by NBA, New Delhi and has secured 'A' grade from NAAC, Bangalore with CGPA of 3.27. Recently, academic autonomous status is also conferred to Institute under Shivaji University by UGC, New Delhi on December 2020.

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- To suggest panel of names to the Academic Council for appointment of examiners
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We are pleased to inform you that you have been nominated by our Institute and very much interested to receive your scholarly guidance as member for BoS of Computer Science and Engineering department .We strongly believe that our Department will nurture under your direction and experience. Hence we kindly request you accept and give your consent for membership. Your valuable guidance and suggestions will surely enhance the curriculum standards.

Thanking You

  
Dean Academics

  
BoS Chairman

  
Principal



Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

---

## Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

---

Nilesh Ratnaparkhi <nilesh.ratnaparkhi@iauro.com>

Wed, Mar 24, 2021 at 8:02 PM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Cc: "Ganesh V. Patil" <gvpatil@tkietwarana.ac.in>, Principal TKIET <principal@tkietwarana.ac.in>, "Ramgonda B. Patil" <rbpatil@tkietwarana.ac.in>, deanacademic@tkietwarana.ac.in

Ack. Thanks for considering me for this. Allow me day time to go through this. You can expect positive response from my side by tomorrow eod.

-Nilesh

[Quoted text hidden]

--

Sent from iPhone



Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

---

## Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

---

Nilesh Ratnaparkhi <nilesh.ratnaparkhi@iauro.com>

Fri, Apr 2, 2021 at 7:03 PM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Cc: "Ganesh V. Patil" <gvpatil@tkietwarana.ac.in>, Principal TKIET <principal@tkietwarana.ac.in>, "Ramgonda B. Patil" <rbpatil@tkietwarana.ac.in>, deanacademic@tkietwarana.ac.in

Dear Prof Patil,

Thanks for considering me for this role. I accept this role.

Please let me know the next steps.

My apologies for late reply, was busy with some personal priorities.

-Nilesh

[Quoted text hidden]

--

Sent from iPhone





Shree Warana Vibhag Shikshan Mandal's

# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

WARANANAGAR, Pin- 416113. Tal. Panhala, Dist. Kolhapur, M.S., India.

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Recognized by Govt. of Maharashtra ; Approved by A.I.C.T.E., New Delhi and Affiliated to Shivaji University, Kolhapur

## An Autonomous Institute

Ref. TKIET/ 2020-21/Autonomous/350

Date: 20-03-2021

To

**Dr. Sachin S. Patil**

Assistant Professor

Rajarambapu Institute of Technology, Rajaramnagar

**Subject:** Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'.

Respected Sir,

**Greetings from Department of Computer Science & Engineering and TKIET, Warananagar...!!!**

The Institute was established in 1983 by visionary leader Late Shri.Tatyasaheb Kore, an architect of Warana cooperative and educational movement. TKIET has been accredited twice by NBA, New Delhi and has secured 'A' grade from NAAC, Bangalore with **CGPA of 3.27**. Recently, academic autonomous status is also conferred to Institute under Shivaji University by UGC, New Delhi on December 2020.

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- To coordinate research, teaching, extension and other Academic activities in the department / Institute.

We are pleased to inform you that you have been nominated by our Institute and very much interested to receive your scholarly guidance as member for BoS of Computer Science and Engineering department .We strongly believe that our Department will nurture under your direction and experience. Hence we kindly request you accept and give your consent for membership. Your valuable guidance and suggestions will surely enhance the curriculum standards.

Thanking You

Dean Academics

BoS Chairman

Principal



---

## Regarding the consent on 'Invitation as member on Board of Studies (BOS) of Computer Science and Engineering Department of TKIET under Autonomous Status'

---

**Sachin Patil** <sachin.patil@ritindia.edu>

Sat, Mar 20, 2021 at 12:43 PM

To: "Head, Department of Computer Science and Engineering" <hodcse@tkietwarana.ac.in>

Cc: Principal TKIET <principal@tkietwarana.ac.in>, deanacademic@tkietwarana.ac.in, "Ganesh V. Patil" <gvpatil@tkietwarana.ac.in>, "Ramgonda B. Patil" <rbpatil@tkietwarana.ac.in>

Dear Sir,

First of all, I am very thankful to TKIET & my dept for providing this opportunity to share my expertise.


I provide my consent to accept your invitation & work for BOS activities. I assure my fullest during this tenure.

Thanking you!!

[Quoted text hidden]



Head, Department of Computer Science and Engineering <hodcse@tkietwarana.ac.in>

<b>Name of the Faculty</b>	DR SANTOSH L DESHPANDE			
<b>Faculty-ID</b>	VO4-5002			
<b>Designation</b>	PROFESSOR			
<b>Date of Joining</b>	12/12/2013			
<b>Gender</b>	Male			
<b>Appointment Type</b>	Regular			
<b>Institute Name</b>	DEPARTMENT OF CSE, VTU, BELAGAVI -590018			
<b>Academic Profile</b>	<b>UG</b>	<b>PG</b>	<b>PhD</b>	
	B.E(E&C)	M. Tech(CSE)	CSE	
<b>Total Experience in Years</b>	<b>Teaching</b>	<b>Industry</b>	<b>Research</b>	
	17 YEARS	4 YEARS	----	
<b>Research Profile</b>				
<b>Research areas</b>	Distributed System, Network Security, Computer Networking, Architecture, Cloud Computing, Decision Support System			
<b>Projects/Thesis (UG/PG/Ph.d)</b>	<b>PG</b>		<b>Ph. D</b>	
	<b>Guided</b>	<b>On-going</b>	<b>Guided</b>	<b>On-going</b>
	60	1	02(Completed)	06
<b>Papers Published in journals</b>	<b>National</b>		<b>International</b>	
	----		33	
<b>Papers Presented in Conferences/workshops</b>	<b>National</b>		<b>International</b>	
	----		08	
<b>Books Published / IPRs/ Patents</b>	03 (Book Chapters)			
<b>H-index</b>	<b>Scopus</b>	<b>Google-Scholar</b>	<b>Other</b>	
	1	2	---	

<b>Professional Profile</b>	
<b>Professional Memberships</b>	LMISTE
<b>Consultancy Projects(if any)</b>	---
<b>Awards(If any)</b>	---
<b>Grants fetched</b>	---
<b>Interaction with Professional Bodies/Others</b>	---



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Personal Information
Expertise Information
Experience
Education Qualification
Publications

#### Academic Identity



#### Scopus Id

57191346105 (<http://www.scopus.com/authid/detail.url?authorId=57191346105>)



#### Google Scholar Id

dx\_qchoAAAAJ&hl=en ([http://scholar.google.co.in/citations?user=dx\\_qchoAAAAJ&hl=en](http://scholar.google.co.in/citations?user=dx_qchoAAAAJ&hl=en))

Vidwan-ID : 111197

Edit Profile



#### Dr Santosh L. Deshpande



Professor

Visvesvaraya Technological University




#### Publications 2016 - 2020






### Publications

2 Journal Articles	10 Conference Proceedings	 1	 10
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### Citations / H-Index

 8 CITATIONS
 1 H-INDEX
 3 CITATIONS

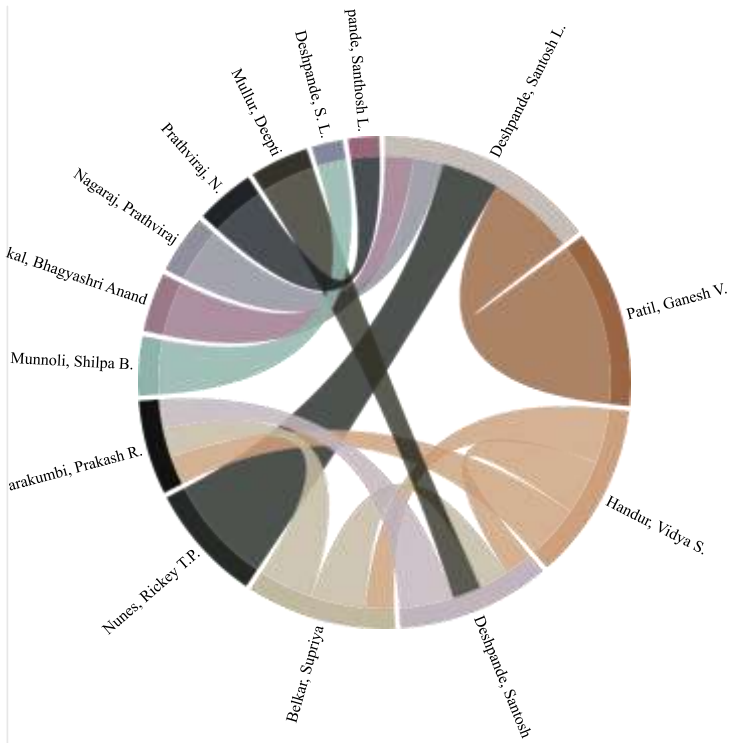
### Google Scholar

 CITATION	 H INDEX	 I-10 INDEX
39 Total	3 Total	1 Total
34 (2015-)	3 (2015-)	1 (2015-)

### Co-author Network Co-author - 8

#### Years

- 2020
- 2019
- 2018
- 2017
- 2016
- 2015
- 2014
- 2013
- 2012
- 2011
- 2010



### 💡 Expertise

#### Computer Science Software Engineering

⚙️ Distributed System, Network Security, Computer Networking, Architecture, Cloud Computing, Decision Support System

### 👤 Personal Information

#### Dr Santosh L. Deshpande

👤 Male

🏠 Dept. of Computer Science and Engineering Visvesvaraya Technological University, Machhe, Belgaum, Karnataka, India

📍 Belagavi, , India - 590018

🌐 [www.vtu.ac.in](http://www.vtu.ac.in) ([www.vtu.ac.in](http://www.vtu.ac.in))

### 👜 Experience

2013 - Present  Professor

Department of Computer Science and Engineering  
Visvesvaraya Technological University


## Qualification

 Ph.D

## Publications (12)

Sort by ▾

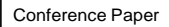
### Rough set based QoS enabled multipath source routing in MANET

 International Journal of Electrical and Computer Engineering, **Volume** 10, **Year** 2020, **Pages** 1915-1923  
DOI:10.11591/ijece.v10i2.pp1915-1923 (<http://dx.doi.org/10.11591/ijece.v10i2.pp1915-1923>)



### Image retargeting using dynamic load balancing-based parallel architecture

Patil, Ganesh V.;Deshpande, Santosh L.

 Advances in Intelligent Systems and Computing, **Volume** 898, **Year** 2019, **Pages** 327-335  
DOI:10.1007/978-981-13-3393-4\_34 ([http://dx.doi.org/10.1007/978-981-13-3393-4\\_34](http://dx.doi.org/10.1007/978-981-13-3393-4_34))

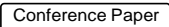


([https://badge.dimensions.ai/details/doi/10.1007/978-981-13-3393-4\\_34?domain=https://vtu.irins.org](https://badge.dimensions.ai/details/doi/10.1007/978-981-13-3393-4_34?domain=https://vtu.irins.org))



### Study of load balancing algorithms for Cloud Computing

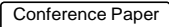
Handur, Vidya S.;Belkar, Supriya;Deshpande, Santosh;Marakumbi, Prakash R.

 Proceedings of the 2nd International Conference on Green Computing and Internet of Things, ICGCIoT 2018, **Year** 2018, **Pages** 173-176  
DOI:10.1109/ICGCIoT.2018.8753091 (<http://dx.doi.org/10.1109/ICGCIoT.2018.8753091>)



### Entropy based performance comparison of cryptographic algorithms on emoticons

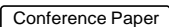
Munnoli, Shilpa B.;Deshpande, S. L.

 Proceedings of the 2017 International Conference On Smart Technology for Smart Nation, SmartTechCon 2017, **Year** 2018, **Pages** 425-428  
DOI:10.1109/SmartTechCon.2017.8358409 (<http://dx.doi.org/10.1109/SmartTechCon.2017.8358409>)



### Hybridization of B-Tree and HashMap for optimized search engine indexing

Jantkal, Bhagyashri Anand;Deshpande, Santosh L.

 Proceedings of the 2017 International Conference On Smart Technology for Smart Nation, SmartTechCon 2017, **Year** 2018, **Pages** 401-404  
DOI:10.1109/SmartTechCon.2017.8358404 (<http://dx.doi.org/10.1109/SmartTechCon.2017.8358404>)





(<https://badge.dimensions.ai/details/doi/10.1109/SmartTechCon.2017.8358404?domain=https://vtu.irins.org>)



## Dynamic load balancing in image retargeting using pipeline architecture

Patil, Ganesh V.;Deshpande, Santosh L.

Article Periodicals of Engineering and Natural Sciences, **Volume 6, Year 2018, Pages 243-264**  
DOI:10.21533/pen.v6i2.270 (<http://dx.doi.org/10.21533/pen.v6i2.270>)



## Context-aware network for smart city services: A layered approach

Nagaraj, Prathviraj;Deshpande, Santosh L.

Conference Paper Communications in Computer and Information Science, **Volume 876, Year 2018, Pages 113-119**  
DOI:10.1007/978-981-13-1423-0\_13 ([http://dx.doi.org/10.1007/978-981-13-1423-0\\_13](http://dx.doi.org/10.1007/978-981-13-1423-0_13))



## Dynamic data placement and tool assignment for big-data orchestrated bioinformatics workflows

Nunes, Rickey T.P.;Deshpande, Santosh L.

Conference Paper 2017 International Conference on Advances in Computing, Communications and Informatics, ICACCI 2017, **Volume 2017-January, Year 2017, Pages 1-6**  
DOI:10.1109/ICACCI.2017.8125807 (<http://dx.doi.org/10.1109/ICACCI.2017.8125807>)



## Distributed rendering system for 3D animations with Blender

Patil, Ganesh V.;Deshpande, Santosh L.

Conference Paper 2016 IEEE International Conference on Advances in Electronics, Communication and Computer Technology, ICAECCT 2016, **Year 2017, Pages 91-98**  
DOI:10.1109/ICAECCT.2016.7942562 (<http://dx.doi.org/10.1109/ICAECCT.2016.7942562>)

Citations



6

Citations



1



(<https://badge.dimensions.ai/details/doi/10.1109/ICAECCT.2016.7942562?domain=https://vtu.irins.org>)

## Rough set approach for solving mult-constrained QoS routing problems in MANET

Prathviraj, N.;Deshpande, Santhosh L.

Conference Paper Proceedings of the 2016 IEEE International Conference on Wireless Communications, Signal Processing and Networking, WiSPNET 2016, **Year 2016, Pages 1150-1153**  
DOI:10.1109/WiSPNET.2016.7566316 (<http://dx.doi.org/10.1109/WiSPNET.2016.7566316>)

Citations



1

Citations



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(<https://badge.dimensions.ai/details/doi/10.1109/WiSPNET.2016.7566316?domain=https://vtu.irins.org>)

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Shree Warana Vibhag Shikshan Mandal's

# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

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Recognized by Govt. of Maharashtra ; Approved by A.I.C.T.E., New Delhi and Affiliated to Shivaji University, Kolhapur

## An Autonomous Institute

Ref. TKIET/ CSE/BoS Meeting No. 1/2020-21/448

Date: 09-Apr-2021

To  
**Dr. S. L. Deshpande**  
HoD, CNE  
VTU, Belgavi

**Subject:** Invitation for Board of Studies Meeting No.1

Respected Sir,  
Greetings from CSE Department and TKIET, Warananagar !!!

With reference to above mentioned subject, we are glad to invite you for the 1<sup>st</sup> meeting of the Board of Studies through the online mode. Therefore, you are humbly requested to make yourself available to attend the meeting as a BoS Member and guide us.

Kindly acknowledge the same.

Meeting details are as follows:

<b>Day &amp; Date</b>	<b>:- Tuesday, 20-April-2021</b>
<b>Time</b>	<b>:- 10:30 AM – 12:30 PM</b>
<b>Mode of Meeting</b>	<b>:- Online (Google Meet)</b>

### Agenda

- 1.1 Greeting and Welcoming the BoS members
- 1.2 Introduction of the BoS Members
- 1.3 Discuss the objectives of BoS and role of BoS members
- 1.4 Review of existing B.Tech (CBCS) Shivaji University syllabus structure
- 1.5 Presentation of proposed B.Tech (Autonomous) syllabus structure w.e.f 2021-22
- 1.6 Inviting suggestions from BoS members for proposed syllabus structure
- 1.7 Recommendation of text books and reference books
- 1.8 Methodologies for teaching-learning and evaluation
- 1.9 Approval of S.Y.B.Tech syllabus structure to be implemented w.e.f 2021-22
- 1.10 Any other point with the permission of the Chair

  
**Prof. R.B. Patil**  
BoS, Member Secretary



  
**Prof. A.G. Patil**  
BoS Chairman & HoD, CSE

# Dr. Mrs. Vandana Shridhar Inamdar

**Designation:** Associate Professor

**Department:** Computer & IT

**Email:** [vhj.comp@coep.ac.in](mailto:vhj.comp@coep.ac.in) / inamdar.vandana6@gmail.com

**Phone Number:** 25507093

## Qualifications:

---

Ph.D Electronics and Telecommunication from University of Pune, 2013

M.E. Electronics ( Computer Specialization) from Pune University, 1997

B.E. Electronics from Shivaji University, 1989

## Date of Joining COEP:

---

Friday, November 26, 1999

## Prior to joining COEP Industrial Experience (Years):

---

2

## Prior to joining COEP Teaching/Research Experience (Years):

---

8

## Area of Research:

---

Signal and image processing, 3d Imaging, lightweight cryptography, Machine learning for computer vision

## Courses Taught:

---

- Data structure
- Fundamental of Computer Programming
- Digital Electronics,
- Advanced Computer Architecture ,
- Multimedia Techniques,
- Digital signal processing
- Discrete mathematics,
- Probability Statistics and Queuing theory

## Responsibilities Shouldered:

---

- worked as a head of the department during July 2015 to June 2018
- Worked as a organizing chair of IEEE and ACM conference
- Incharge of technical evaluation of Computing devices purchase committee
- Worked as PG NBA Coordinator
- Worked as PG Coordinator
- Worked as member of local inspection committee of Pune iniversity

- Worked as a member of inspection committee appointed by Director of Technical Education, Mumbai
- Worked as member of selection committee appointed by University of Pune
- Worked as in-charge of various co curricular activities of students.
- Played a major role in industry institute interaction.

### **Number of PhD/ M.Tech/ B.Tech students guided:**

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Phd: 6(Ongoing)

M.Tech:50

B.Tech: 125

### **Research Project & Publication Details:**

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Completed:

- Research project funded by All India Council of Technical Education titled "Authentication of digital media through biometric watermarking" Year 2012 Rs.3.58 lakhs.
- MODROB project on " Advanced Computer graphics" Year 2008, Rs.8 lacs.
- NVIDIA CTC grant Rs.1 lakhs

Publications

- International level: Conferences-16; Journals: 20
- National level: Conferences-03
- Citations : 284, H-Index- 10

### **Memberships and Affiliations:**

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- Member, IEEE
- Member, IET
- Life Member, ISTE
- Life Member, CSI

### **Current Projects :**

---

Projects Ongoing

Research project funded by Department of Science and technology, ICPS division "Design and Development of Adaptive Energy Efficient Security Mechanisms for Mobile Computing Devices, Year 2018, Rs.15 lakhs.

### **Teaching Experience:**

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29

### **Research Experience:**

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5





Shree Warana Vibhag Shikshan Mandal's

# TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY

WARANANAGAR, Pin- 416113. Tal. Panhala, Dist. Kolhapur, M.S., India.

Phone : 02328-224012, 224013, Fax : 02328-223507

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Recognized by Govt. of Maharashtra ; Approved by A.I.C.T.E., New Delhi and Affiliated to Shivaji University, Kolhapur

## An Autonomous Institute

Ref. TKIET/ CSE/BoS Meeting No. 1/2020-21/448

Date: 09-Apr-2021

To

**Dr.(Mrs.) V.S. Inamdar**

Associate Professor,  
College of Engineering, Pune  
Shivajinagar, Pune-411 005.

**Subject:** Invitation for Board of Studies Meeting No.1

Respected Madam,  
Greetings from CSE Department and TKIET, Warananagar !!!

With reference to above mentioned subject, we are glad to invite you for the 1<sup>st</sup> meeting of the Board of Studies through the online mode. Therefore, you are humbly requested to make yourself available to attend the meeting as a BoS Member and guide us.


Kindly acknowledge the same.

Meeting details are as follows:


<b>Day &amp; Date</b>	: - Tuesday, 20-April-2021
<b>Time</b>	: - 10:30 AM – 12:30 PM
<b>Mode of Meeting</b>	: - Online (Google Meet)

### Agenda

- 1.1 Greeting and Welcoming the BoS members
- 1.2 Introduction of the BoS Members
- 1.3 Discuss the objectives of BoS and role of BoS members
- 1.4 Review of existing B.Tech (CBCS) Shivaji University syllabus structure
- 1.5 Presentation of proposed B.Tech (Autonomous) syllabus structure w.e.f 2021-22
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- 1.7 Recommendation of text books and reference books
- 1.8 Methodologies for teaching-learning and evaluation
- 1.9 Approval of S.Y.B.Tech syllabus structure to be implemented w.e.f 2021-22
- 1.10 Any other point with the permission of the Chair

  
**Prof. R.B. Patil**  
BoS, Member Secretary



  
**Prof. A.G. Patil**  
BoS Chairman & HoD, CSE





## **PROF. (Dr.) DATTATRAYA V. KODAVADE**

- **Designation :** Head of Department & Dean (Projects and Consultancy)  
Professor in Computer Sc. & Engineering
- **Qualification :** B.E., M.E., Ph. D.
- **Experience :** Teaching : 28 years
- **Email id :** [dvkodavade@gmail.com](mailto:dvkodavade@gmail.com)
- **Interest Areas :** Artificial Intelligence & Knowledge Based Systems, IoT
- **Date of joining :-** 20/07/1992
- **Papers published in Journals :-**National :07 International :10
- **Papers presented in Conference:-**National :15 International : 05
- **Ph.D. Guide:-**Yes
- **Field:-**Artificial Intelligence ,Neural Networks, Hybrid Intelligence
- **University :-**Shivaji University, Kolhapur
- **Student Guidance:-**PG : 12 PhD : 06
- **Training :-**10
- **Workshop :-**15
- **Seminars :-**10
- **Book Published :-**02
- **Professional Memberships:-**
  - Life Member of Indian Society for Technical Education (ISTE)
  - Member of Association for Computing Machinery (ISTE)
  - Member of IEEE Computer Society
- **Consultancy Activities :-**Various Banks and Industries
- **Awards :-**Infosys Outstanding Performance Award
- **Grants fetched :-**8 Lakhs
- **Interaction with Professional Institutions :-**IIT Bombay, Georgia University, USA, IIT Chennai, ACM India
- **Others :-**IDAACS Germany, WORLDCOM USA, Ministry of Shenzhen China



Shree Warana Vibhag Shikshan Mandal's

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## An Autonomous Institute

Ref. TKIET/ CSE/BoS Meeting No. 1/2020-21 / 448

Date: 09-Apr-2021

To

**Dr. D. V. Kodavade**

Professor and Head,

Department of Computer Science & Engineering,

DKTE Ichalkaranji.

**Subject:** Invitation for Board of Studies Meeting No.1

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**Prof. R.B. Patil**  
BoS, Member Secretary



**Prof. A.G. Patil**  
BoS Chairman & HoD, CSE

## Contact

[www.linkedin.com/in/dinesh-kothawade-aaa15a9](http://www.linkedin.com/in/dinesh-kothawade-aaa15a9) (LinkedIn)

## Top Skills

SAP SD

SAP

SAP R/3

## Certifications

Onward and Upward Program

# Dinesh Kothawade

Service Delivery Manager @ TCS  
Pune

## Summary

Dinesh is experienced IT professional having 22+ years of broad industry experience, strong analytical and hands-on technical skills in SAP SD, VMS, IS Oil, IS Auto, More than 7 years of overseas onsite – offshore model working experience. 15 years of SAP Functional consultancy Experience with 4 Complete Life Cycle Implementation in IS OIL through Business Blue print, Realization, Integration testing and Go Lives. 4 Production support and 7 Rollout in Europe area. One Upgrade (Merger of 2 big Oil companies with ERP upgrade) in CANADA.

Dinesh focus is on delivering solutions to unique problems facing mainly in Manufacturing, Oil & Gas, Automotive, Process and Chemical, Retail Industry

Responsible for leading IT strategic initiatives that align with enterprise objectives to deliver measurable business value utilizing global delivery model (Worked in North America, Europe and Asia). Good amount of cross functional knowledge across FI/CO, MM, and PP Modules with through understanding of ABAP.

Key to his success is his ability to effectively engage client personnel, lead cross functional teams and encourage thought leadership to achieve desired project/program objectives. Always met very stringent deadlines and delivered results even under high pressure. Constant desire to learn; genuine commitment to excel.

Specialties: • Extensive experience in Implementations, Roll-out & Application Support projects across industries.

SAP IS AUTO : VMS, WARRANTY MANAGEMENT, SPARE PARTS

SAP IS-Oil (downstream): TSW, EXG, TD, TDP HPM

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## Experience

Tata Consultancy Services

10 years 1 month

SAP Project Manager

April 2014 - Present (7 years 1 month)

Hinjewadi Phase III

Integration Manager

April 2011 - March 2014 (3 years)

Mumbai Area, India

IBM India Pvt.Ltd.

Sr.IS OIL Consultant

June 2004 - April 2011 (6 years 11 months)

Responsible for solutioning, designing and presenting a range of solutions around SAP IS OIL downstream business for prospective Client .

Responsible for Transitioning the Projects from different Vendor and build a Support team around SAP IS OIL downstream business .

Designing and delivering solutions, along with the services proposal content and pricing for existing customer Enhancemnets.

Vivitar Electronics

Territory Manager

October 2000 - May 2004 (3 years 8 months)

An ISO 9001: 2000 Company Manufacturing & Marketing of high end Power Equipments and National Level Power Solution Provider for Imported make of "POWERWARE" UPS system. Manufacturing UPS ranging from 500VA to 100KVA. The company is catering the needs of Power Industry by complete installation & commissioning of their Power Distribution network, having annual Turnover for more than 3crs. The corporate office is at Govandi, Mumbai and having Regional offices at Goa, Pune, Hydrabad. Major customer-DHL World Wide Express, Seimens, CIPLA LTD and others.

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## Education

Institute for personal Leadership

Resilient Leadership · (2018 - 2018)

North Maharashtra University

MBA, Marketing · (1996 - 1998)

North Maharashtra University

BE ( ELEX), Electronics · (1991 - 1996)





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## An Autonomous Institute

Ref. TKIET/ CSE/BoS Meeting No. 1/2020-21/448

Date: 09-Apr-2021

To  
**Mr. Dinesh Kothavade**  
Project Manager,  
TCS Pune.

**Subject:** Invitation for Board of Studies Meeting No.1

Respected Sir,  
Greetings from CSE Department and TKIET, Warananagar !!!

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- 1.10 Any other point with the permission of the Chair

  
**Prof. R.B. Patil**  
BoS, Member Secretary



  
**Prof. A.G. Patil**  
BoS Chairman & HoD, CSE

## Contact

[www.linkedin.com/in/hrishikesh-hirve-306b3122](http://www.linkedin.com/in/hrishikesh-hirve-306b3122) (LinkedIn)

## Top Skills

SDLC

Requirements Analysis

DB2

## Certifications

Certified SAFe® 5 Program Consultant

SAFe Agilist Certification

Certified Scrum Product Owner (CSPO)

Certified Scrum Master

# Hrishikesh Hirve

Senior Manager at Capgemini - Agile enthusiast / Coach / Trainer | Kanban Practitioner | Scrum master | Project Manager

Pune

## Summary

Experienced Scrum Master / Agile Coach / Project Manager with a demonstrated working history with Hands-on experience in Agile transformation in different domains (such as Banking, Insurance ) in the Information technology and Services industry. With a successful stint of an extensive hands-on experience of delivering complex projects in high pressure environments, he has a proven achievement of obtaining desired results without losing the focus on team spirits simultaneously meeting customer expectations.

With most recent position of Agile Lead / Coach in Agile implementation with multiple large organizations, he has also worked as Scrum Master delivering multiple high priority Banking projects applying Scrum, Kanban working with Cross-located, Cross Functional teams.

---

## Experience

### Capgemini

4 years 5 months

#### Senior Manager

July 2020 - Present (9 months)

Pune, Maharashtra, India

#### Manager

November 2016 - June 2020 (3 years 8 months)

Pune, Maharashtra, India

### Principal Financial Group

9 years 9 months

#### Delivery Manager

July 2015 - October 2016 (1 year 4 months)

Pune

## Consultant

February 2007 - October 2016 (9 years 9 months)

Pune Area, India

## Kanbay

Associate Consultant

January 2004 - February 2007 (3 years 2 months)

---

## Education

Nowrosjee Wadia College, Pune

M. Sc., Master of Science (Computers) · (2002 - 2004)

Annasaheb Magar College, Hadapsar

B. C. S, Bachelor of Computer Science · (1999 - 2002)

Nowrosjee Wadia College, Pune

H. S. C · (1997 - 1999)

Jnana Prabodhini Prashala

S. S. C · (1993 - 1997)





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## An Autonomous Institute

Ref. TKIET/ CSE/BoS Meeting No. 1/2020-21 /44/8

Date: 09-Apr-2021

To  
**Mr. Hrishikesh Hirve**  
Senior Manager,  
Capegemini, Pune

**Subject:** Invitation for Board of Studies Meeting No.1

Respected Sir,  
Greetings from CSE Department and TKIET, Warananagar !!!

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- 1.10 Any other point with the permission of the Chair

  
**Prof. R.B. Patil**  
BoS, Member Secretary



  
**Prof. A.G. Patil**  
BoS Chairman & HoD, CSE



## Contact

[www.linkedin.com/in/nileshratnaparkhi](http://www.linkedin.com/in/nileshratnaparkhi) (LinkedIn)  
[www.iauro.com](http://www.iauro.com) (Company)  
[twitter.com/inilesh](https://twitter.com/inilesh) (Other)

## Top Skills

iPhone development  
Hadoop  
HBase

# Nilesh Ratnaparkhi

CTO @ iauro. We are hiring.  
Pune

## Summary

A warehouse of Tech updation, Nilesh's superpower lies in connecting a progressive solution to redundant problems. Having laid the cornerstone for iauro he has consistently worked towards building it into a strong enterprise. From enterprise solutions to being THE ENTERPRISE FOR SOLUTIONS, iauro has benefitted from his agile growth framework.

Understanding the intricacies of enterprise process' Nilesh is often found fine tuning and realigning his solutions to custom fit his client's needs. Growing tech expertise, building a sound capable team, recognizing merit, identifying groundbreaking solutions to existing enterprise problems are some of his habitual constants.

His tech stack is quite extensive and includes - relational database internals, microservices, develops and design thinking.

An ardent fan of culture and cuisine, Nilesh is always up for sharing his notes over the same, but beware, he might just sneak in some tech talk there.

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## Experience

iauro systems Pvt Ltd

12 years 9 months

Chief Technology Officer

January 2009 - Present (12 years 5 months)

Pune Area, India

Founder

September 2008 - Present (12 years 9 months)

Working on iPhone application development.

Persistent Systems

Senior Software Engineer

July 2005 - August 2008 (3 years 2 months)

job description:

I am part of the Development Engineering team (Optimizer team) for well known parallel database system. I have been involved in project execution and bug fixing related to the Query Optimizer module.

career skills:

- Experience in database internals, especially in query optimization.
- Good knowledge of query optimization, query re-write, query de-correlation and query planning.
- Good knowledge about massively parallel database systems

Netezza

Software Engg

2005 - 2008 (3 years)

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## Education

Savitribai Phule Pune University

BE, CS · (2002 - 2005)



Shree Warana Vibhag Shikshan Mandal's

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## An Autonomous Institute

Ref. TKIET/ CSE/BoS Meeting No. 1/2020-21/448

Date: 09-Apr-2021

To

**Mr. Nilesh Ratnaparkhi**  
Chief Technology Officer,  
iauro Systems Pvt.Ltd , Pune

**Subject:** Invitation for Board of Studies Meeting No.1

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**Prof. R.B. Patil**  
BoS, Member Secretary



  
**Prof. A.G. Patil**  
BoS Chairman & HoD, CSE



**PROF. (Dr.) Dr. S. S. Patil**

**Department:**Computer Science & Engineering

**Email:**[sachin.patil@ritindia.edu](mailto:sachin.patil@ritindia.edu)

Education

**Ph. D. (CSE), QIP Scholar**

Walchand College of Engineering, Sangli MH

**M. Tech. (CST) (2010)**

DoT, Shivaji University, Kolhapur MH

**B. E. (CSE) (2003)**

TKIET, Warananagar MH

Experience

Teaching Experience: (13 Years)

**2018-19**

Assistant Professor, Rajarambapu Institute of Technology, Rajaramnagar.

**2011-2015**

Assistant Professor & HOD (CSE), Rajarambapu Institute of Technology, Rajaramnagar.

**2010-2011**

Assistant Professor in CSE, Rajarambapu Institute of Technology, Rajaramnagar.

**2003-2010**

Lecturer in CSE, Rajarambapu Institute of Technology, Rajaramnagar.

**Research Experience (3 Years)**

**2015-2018**

QIP Research Scholar at Walchand CoE, Sangli

Research Area(s)

- Database Engineering and Mining
- Data Analytics

**Professional Distinctions**

- Membership of professional bodies
  - ISTE (Life Member)
- Awards
  - Awarded as “Distinguished Facilitator” by Infosys, Pune under Inspire faculty contest 2013.
  - Best Paper Award - Oral paper at WCE Research Symposium on Computing (RSC) Dec, 2017 (Paper Titled: Handling of Class Imbalanced Problem in Big Data Sets: An Experimental Evaluation (UCPMOT))
- Achievements
  - Awarded Bronze Partnership for consecutive 4 years (2011-2015) by Infosys under Campus Connect program

## **Key Research Projects**

- Worked as a co-PI for In-house research funding project of costing 37,000/- at RIT on "Ontology based information retrieval systems" in 2012-13.
- An Approach towards Capturing user intention based on Visual and Textual Similarity
- A Technique for Automatic Template Extraction From Heterogeneous Web pages
- Framework for a recommendation on large scale web graphs using Heat Diffusion
- Automatic generation of emotions using text mining for social networking websites
- Design & Analysis of Reconfigurable Platform for Frequent Pattern Mining
- Data Extraction for Deep web using Wordnet





Shree Warana Vibhag Shikshan Mandal's

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## An Autonomous Institute

Ref. TKIET/ CSE/BoS Meeting No. 1/2020-21/448

Date: 09-Apr-2021

To

**Dr. Sachin S. Patil**

Assistant Professor

Rajarambapu Institute of Technology, Rajaramnagar

**Subject:** Invitation for Board of Studies Meeting No.1

Respected Sir,

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**Prof. R.B. Patil**  
BoS, Member Secretary



  
**Prof. A.G. Patil**  
BoS Chairman & HoD, CSE

Shree Warana Vibhag Shikshan Mandal's

**TATYASAHEB KORE INSTITUTE OF ENGINEERING AND TECHNOLOGY**

(An Autonomous Institute, Affiliated to Shivaji University, Kolhapur)



Minutes of  
1<sup>st</sup> (First) Board of Studies Meeting  
of  
Department of Computer Science and Engineering  
held on Tuesday, 20<sup>th</sup> April 2021 at 10:30 AM

SWVSM's  
**Tatyasaheb Kore Institute of Engineering  
and Technology, Warananagar** Tal-Panhala Dist-Kolhapur 416113 (Maharashtra)  
(An Autonomous Institute, Affiliated to Shivaji University, Kolhapur)  
**Department of Computer Science and Engineering**

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**Board of Studies Meeting - 01 (First)**

**Day & Date** :- Tuesday, 20-April-2021  
**Time** :- 10:30 AM - 12:30 PM  
**Mode of Meeting** :- Online (Google Meet) for external BoS members  
**Venue** :- Conference Hall

**Agenda**

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- 1.7 Recommendation of text books and reference books
- 1.8 Methodologies for teaching-learning and evaluation
- 1.9 Approval of S.Y.B.Tech syllabus structure to be implemented w.e.f 2021-22
- 1.10 Any other point with the permission of the Chair

**Prof. R.B. Patil**  
BoS, Member Secretary

**Prof. A. G. Patil**  
BoS Chairman & HoD, CSE



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**Department of Computer Science and Engineering**

**Department of Computer Science and Engineering  
Board of Studies**

Sr. No.	Composition	Name & Affiliation	Nominated By	Remark
1.	Head of Department	Prof. A. G. Patil	Principal	Chairman
2.	Member from Senior Faculty within Department (Entire Faculty of Each specialization)	Dr. G. V. Patil	HoD/ Chairman	Member
		Prof. A. S. Phalle		Member
		Prof. D. B. Mirajkar		Member
		Prof. K. K. Awale		Member
		Prof. S. R. Shetake		Member
		Prof. Mrs. B. A. Chougule		Member
		Prof. M. R. Hudagi		Member
		Prof. S. A. Urabinahatti		Member
3.	Two Subject Experts from outside the parent University	Dr. S. L. Deshpande <i>HoD, CNE VTU, Belgavi</i>	Academic Council	Member
		Dr. (Mrs.) V.S. Inamdar <i>Associate Professor, College of Engineering, Pune</i>		Member
4.	One Expert from the panel of 6 recommended members by the College Principal	Dr. D.V. Kodavade <i>Head of Department &amp; Professor in Computer Sc. &amp; Engineering, DKTE, Ichalkaranji</i>	Vice Chancellor, Shivaji University, Kolhapur	Member
5.	One representative from Industry /Corporate sector/allied area related to the placement	Mr. Dinesh Kothavade <i>Project Manager, TCS Pune</i>	Principal/ HoD /TPO	Member
		Mr. Hrishikesh Hirve <i>Senior Manager, Capegemini</i>		Member
		Mr. Nilesh Ratnaparkhi <i>CTO, iairo Systems Pvt.Ltd Pune</i>		Member
6.	One PG meritorious Alumnus	Dr. Sachin Patil <i>Assistant Professor, Rajarambapu Institute of Technology, Rajaramnagar.</i>	Principal/ HoD	Member Expert from outside the college whenever special courses of studies are to be formulated
	Member of staff	Prof. I. I. Tamboli		Member Other Member of staff the same faculty
7.	Senior Faculty within Department	Prof. R. B. Patil	HoD/BoS Chairman	Member Secretary

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MINUTES OF MEETING

The 1<sup>st</sup> (First) Board of Studies Meeting of the Department of Computer Science and Engineering was held on 20<sup>th</sup> April, 2021 at 10.30 AM in the Conference Hall, T.K.I.E.T Warananagar, through Online mode (Google Meet) due to COVID-19 restrictions.  
(Google Meet link: [meet.google.com/veu-oqfs-hfr](https://meet.google.com/veu-oqfs-hfr))

Following BoS members were present during the meeting

Sr. No.	BoS Member	Remark
1.	Prof. A. G. Patil	Present (in-person)
2.	Dr. G. V. Patil	Present (in-person)
3.	Prof. A. S. Phalle	Present (in-person)
4.	Prof. R. B. Patil	Present (in-person)
5.	Prof. D. B. Mirajkar	Present (in-person)
6.	Prof. K. K. Awale	Present (Online)
7.	Prof. S. R. Shetake	Present (Online)
8.	Prof. Mrs. B. A. Chougule	Present (in-person)
9.	Prof. M. R. Hudagi	Present (Online)
10.	Prof. S. A. Urabinahatti	Present (Online)
11.	Prof. R. C. Shikkeri	Present (Online)
12.	Prof. I. I. Tamboli	Present (in-person)
<b>Academic Experts outside the Shivaji University</b>		
13.	Dr. S. L. Deshpande	Present (Online)
14.	Dr.(Mrs.) V.S. Inamdar	Present (Online)
<b>BoS Members as Shivaji University, Kolhapur Nominee</b>		
15.	Dr. D.V. Kodavade	Present (Online)
<b>BoS Members as Industrial Representatives</b>		
16.	Mr. Dinesh Kothavade	Present (Online)
17.	Mr. Hrishikesh Hirve	Present (Online)
18.	Mr. Nilesh Ratnaparkhi	Present (Online)
<b>BoS Member as a PG Meritorious Alumnus</b>		
19.	Dr. Sachin Patil	Present (Online)

As non-BoS members & Institute representatives, below members were also present

Sr. No.	Institute Representatives	Remark
1.	Dr. S. V. Anekar (Principal)	Present (in-person)
2.	Dr. K. I. Patil (Dean Academics)	Present (Online)

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- Agenda 1.1**                      **Greeting and Welcoming the BoS members**
- Resolution**                      As a token of respect and gratitude, **Prof. I. I. Tamboli BoS Member & Assistant Professor in CSE department** welcomed all the members for the first BoS meeting. He also welcomed Principal **Dr. S. V. Anekar** and Dean Academic **Prof. K. I. Patil**. He has touched the agenda of BoS meeting quickly. He also introduced the **T.K.I.E.T. Warananagar** institute at glance.
- Agenda 1.2**                      **Introduction of the BoS Members**
- Resolution**                      **Dr. G. V. Patil** Dean, **Students & Assistant Professor in CSE department**, introduced and welcomed all the external BOS members.
- According to Table Program following members were addressed the BoS Meeting.**
- Hon'ble Dr.S.V.Anekar** Principal **T.K.I.E.T Warananagar** addressed the 1<sup>st</sup> BoS Meeting of CSE department.
- He welcomed all External BoS Members and appealed to all BoS Members to provide valuable suggestions in designing of curriculum.
  - Mentioned the importance of Academic Autonomy status and highlighted difference between affiliated & autonomous status
  - Discussed about the credits for the academic course & honour course.
  - Emphasized that curriculum should pace with industry requirements which makes students employable, develops entrepreneurial skills to face global challenges
  - Gave assurance that our institute will design the dynamic curriculum by using valuable suggestions given by External BoS Members for the upcoming students.

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**Dr.K.I.Patil, Dean Academics** addressed the 1<sup>st</sup> BoS Meeting of CSE department.

- He welcomed all External BoS Members and explained the competencies required by students in current digital era.
- Mentioned the importance of Board of Studies in addition of the new programs, changing the syllabus as per the needs of the global market, review of the newly added programs etc.
- Highlighted about globalization, digitalization & computerization and importance of adopting the changes takes place in the industry to cope up with the competitive market.

**Agenda 1.3**

**Discuss the objectives of BoS and role of BoS members**

Resolution

Address of Prof. A. G. Patil, BoS Chairman & HoD, CSE

- At the outset, he introduced CSE department in glance.
- Briefed the Vision and Mission statements of the CSE department are aligned with the Institute Vision and Mission and introduced the all internal BoS members.
- Highlighted the objectives of BoS and role of BoS members.
- Requested all invitee BoS members to advise the new methodologies for programming subjects.

**Agenda 1.4**

**Review of existing B.Tech (CBCS) Shivaji University syllabus structure by Prof.R.B.Patil BoS, Member Secretary**

Resolution

- Took a review of the existing B.Tech (CBCS) Shivaji University syllabus structure.
- He invited suggestions from all the external BoS members.
- He check listed the 198 credits for CSE department and presented the course wise credits distribution of Shivaji University.
- He defined the goals of Outcome Based Education and Program design that is Top Down Approach.



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**Agenda 1.5**                      **Presentation of proposed B.Tech (Autonomous) syllabus structure  
w.e.f 2021-22 by Prof.R.B.Patil BoS Member Secretary**

**Resolution**

- Presented the proposed draft syllabus structure from FY to Final Year B.Tech along with credits
- Emphasized on in detailed draft syllabus of all the subjects of S.Y.B.Tech which is to be implemented from w.e.f. Oct-2021.

**Agenda 1.6**                      **Inviting suggestions from BoS members for proposed syllabus  
structure**

**Resolution**

The following suggestions have been recommended by the committee with regard to below mentioned points.

**Dr.S.L.Deshapande** emphasized to ponder upon below aspects while designing syllabi for autonomous institute

- Follow the guidelines of **National Education Policy(NEP)**.
- AICTE guidelines for Curriculum design
- **Refer ACM/IEEE** curriculum guidelines as a benchmark for syllabus design
- 5 Year Academic plan must be prepared in advance
- Follow **CBCS** pattern for curriculum.
- Fundamental streams vertical.
- Credits should be considered for Digital Learning
- Institute is independent to set the syllabus under autonomous. AICTE guidelines are not mandatory to follow as it is.
- Frequency of the BoS meeting needs to be mentioned.
- Certification courses must be added and assign the credits.
- Faculty expertise courses need to be added. Need to capture local talent so that faculty can design and execute the course based on his expertise.
- Insisted to go along with **CIE- 50%, ISE-20% and ESE-30%** for evaluation scheme.
- The syllabus should not be designed as per comfort zone of faculty. It should be based on industry needs and outcome bases i.e. what student will acquire - skill or knowledge..?

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**Suggestions on S.Y.B.Tech (Second Year) Syllabus Structure**

**Computer Networks I & II**

- Questioned that is there need of 2 subjects related CN. Instead of having CN-I and CN-II, is it feasible to go for single subject

**Proposed by : Dr.D.V.Kodavade**

**Statistics and Fuzzy Systems**

- To include a new course Computer Mathematics. (Dr.D.V.Kodavade)
- Instead of Statistics and Fuzzy systems, devise only concepts required for CS student and rename it as Mathematics for Computer Science (Dr.D.V.Kodavade)
- Fuzzy part is not required in Statistics and Fuzzy Systems subject (Dr.Mrs.V.K.Inamdar)

**Proposed by : Dr.D.V.Kodavade, Dr.Mrs.V.K.Inamdar**

**Data Structures**

- For Data Structure Laboratory use C++, instead of C (Dr.Sachin Patil)
- Data Structure subjects add contents related to Time and Space Complexity (Dr.Mrs.V.K.Inamdar)

**Proposed by : Dr.Sachin Patil, Dr.Mrs.V.K.Inamdar**

**Computer Organization and Architecture**

- Replace the COA course by other subjects.
- Computer organization and Architecture must be two separate subject
- Latest Architectures need to be added in CO subject.
- Computer organization and Architecture must be two separate subject

**Proposed by : Dr.Sachin Patil**

**Discrete Mathematics Structures**

- Reorder the units DMS Subject
- Combine Tree and graph into single unit.
- Move the probability unit in Statistics Subject
- Switch either DMS or Statistics subject in other semester/year.
- Recommended Kenneth H. Rosen book as text book instead of Reference book.

**Proposed by : Dr.Mrs.V.K.Inamdar**



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**Additional issues proposed by Mr. Dinesh Kothavade**

- Invite more practical based content instead of theory such as mini projects and other creativity.

**Additional issues proposed by Dr.D.V.Kodavade**

- Adding 2 ISEs like ISE-1 and ISE-2 and
- Understanding NPTEL and SWAYAM courses by SY students is very difficult.

**Additional issues proposed by Dr.S.L.Deshpande**

- Absolute grading in syllabi and CIE.
- Instead making SY syllabus more complex and to put more overload on SY students, its needs to be shifted to TY syllabus and students

**Additional issues proposed by Dr.Sachin Patil**

- At the outset He asked what ISA is and evaluation criteria of it.
- To focus on 40% hands-on activity to evaluate the ISA.

**Additional issues proposed by Dr.Mrs.V.K.Inamdar**

- Consider the credits based syllabus not marks based
- Honour course credits must be between 12 and 16.
- MOOC based courses must be added.

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**Suggestions on T.Y.B.Tech (Third Year) Syllabus Structure**

**Computer Graphics**

- Instead of Computer Graphics, any Programming Language needs to be considered as a Open Elective (**Dr.S.L.Deshpande**)
- Instead of Computer Graphics subject include Multimedia subject/topic (**Dr.D.V.Kodavade**)

**Proposed by : Dr.S.L.Deshpande, Dr.D.V.Kodavade**

**Linux Operating Systems**

- He suggested change the name of subject as Operating System instead of Linux Operating System

**Proposed by : Dr.D.V.Kodavade**

**Modeling and Design Lab**

- Instead of Modelling and Design Lab (Looks like Mechanical Department Lab) name it as Object Oriented Modeling Design. (**Dr.D.V.Kodavade**)
- **Mr.Nilesh Ratnaparakhi** suggested the to add following topics in Practical Sessions,
  - Cloud Native topic, which is essential to industry for development of Products and referring Cloud Native as Container
  - Best No-Code Development Platforms Software.
  - Align and Typescript
  - A Novel Technique of Optimization for the COCOMO for modeling

**Proposed by : Dr.D.V.Kodavade, Mr.Nilesh Ratnaparakhi**

**System Programming**

- Rename the System Programming as System Softwares

**Proposed by Dr.Sachin Patil**

**Design and Analysis of Algorithms**

- Instead of Design and Analysis of Algorithms, rename it as Computer Algorithms

**Proposed by Dr.Sachin Patil**

**Additional issues proposed by Dr.D.V.Kodavade:**

- Make mandatory certifications courses for NPTEL and SWAYAM for MI, AI, and Python.
- Python can be added as MOOC courses.

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**Additional issues proposed by Dr.Sachin Patil:**

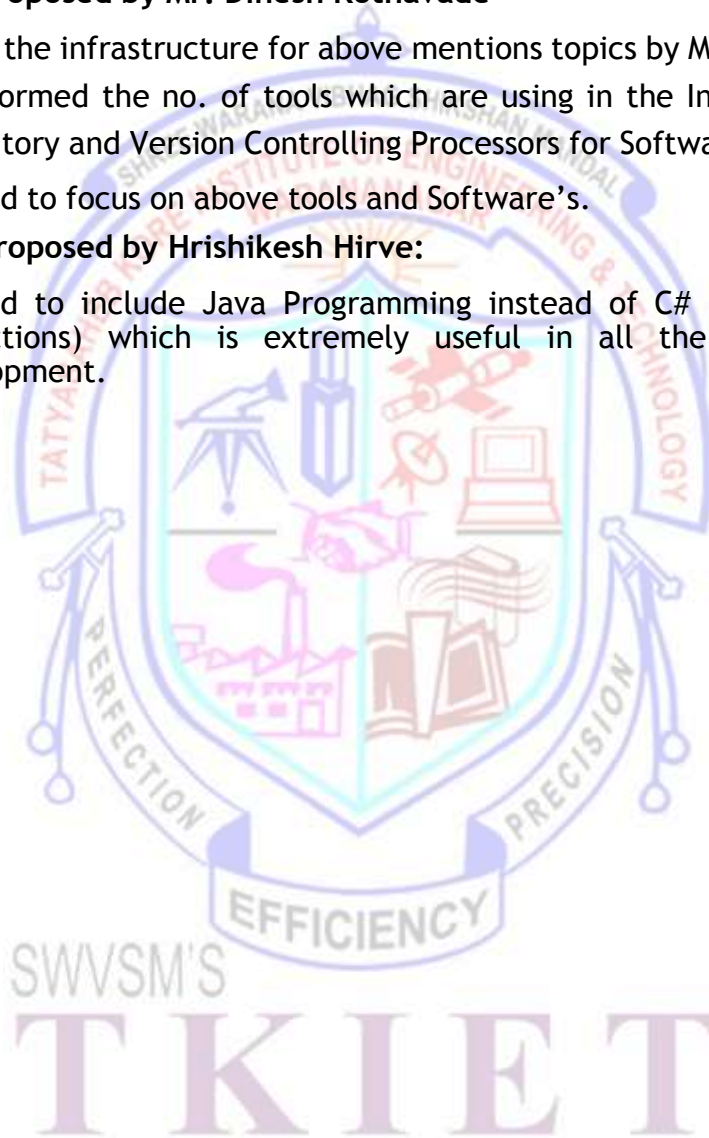
- Consider the Open Elective instead of programming choice.
- He also insisted to include Self Learning Courses to Third Year Students.

**Additional issues proposed by Mr. Dinesh Kothavade**

- Check the infrastructure for above mentions topics by Mr.Nilesh.
- He informed the no. of tools which are using in the Industry like DevOps, Repository and Version Controlling Processors for Software development
- Insisted to focus on above tools and Software's.

**Additional issues proposed by Hrishikesh Hirve:**

- Insisted to include Java Programming instead of C# (as C# having some restrictions) which is extremely useful in all the area of Software development.



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**Suggestions on Final Year B.Tech Syllabus Structure**

**Advance Computer Architecture (ACA)**

- For ACA Subjects include all new architectures. (Dr. Sachin Patil)
- Include GPU Udaan in ACA course. (Dr.Mrs.V.K.Inamdar)

Proposed by Dr. Sachin Patil, **Dr.Mrs.V.K.Inamdar**

**Software Testing and Quality Assurance (STQA)**

- Recommended to include STQA subject as an Open Elective, not core subject.

Proposed by **Dr. Sachin Patil**

**Additional issues proposed by Dr.Sachin Patil**

- Include Case Studies and Projects.
- He insisted to design the courses semester wise such that students will learn the subjects semester by semester (sequentially) and do not allows the gaps.
- Add some MOOC courses like Processing with LATEX or report preparation using LATEX
- Emphasis must be given to continuous evaluation so, percentage marks of ESE and ISE (Proposed structure is having ESE-70% and ISE-30%)  
Must be: ESE 50% , ISE- 50%

**Additional issues proposed by Dr.S.L.Deshpande:**

- Design the 8<sup>th</sup> semester syllabus such that which covers the industry needs (up to 90%)

**Agenda 1.7 Recommendation of text books and reference books**

**Resolution**

- Dr. Mrs. V. S. Inamdar has suggested Discrete Mathematical Structure by Kenneth H.Rosen should be added as a Text Book. and also insisted to combine COA and OS Subjects.



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**Agenda 1.8 Methodologies for teaching-learning and evaluation**

Resolution

- Prof.Dr.Sachine Patil: He informed to add 33% active Teaching Learning Methods and Brainstorming.
- All the External BoS members suggested for Continuous Internal Evaluation(CIE) methodologies must be adopted.s

**Agenda 1.9 Approval of S.Y.B.Tech syllabus structure to be implemented w.e.f 2021-22**

Resolution

- With above suggestions, the proposed syllabus structure, subjects and its contents are approved by all the members of the BoS to be implemented by Oct-2021-22.

**Agenda 1.10 Any other point with the permission of the Chair**

Resolution

- Finally Dr.G.V.Patil informed to all External BOS members that, in this BoS meeting we were focusing on second year syllabus and we will form progressive changes in third and final year curriculum.

Meeting concluded with Vote of Thanks by Prof.R.C.Shikkeri Assoc.TPO & BoS Member and formally announced the conclusion of BoS Meeting.

SWVSM'S  
**TKIET**

Dr. K.I. Patil  
Dean Academics

Prof.A.G.Patil  
BoS Chairman & HoD,CSE

Dr.S.V.Anekar  
Principal